1. GENERAL
   1. Fabricate PCB in accordance with IPC-6012B, Class 2 and IPC-6013 when applicable.
   2. Bow and twist shall be 0.75% or less, measured in accordance with IPC-TM-650, method 2.4.22.
   3. Artwork may be adjusted by the PCB fabricator to compensate for manufacturing process tolerances or impedance requirements.
   4. All dimensions are mm.

2. MATERIALS
   1. Laminate and prepreg to be ISOLA 370HR or equivalent.
   2. Copper foil to be in accordance with IPC-4562.

3. HOLES
   1. Hole sizes are finished hole sizes after plating.
   2. No hole breakout allowed.

4. FINISH
   1. Apply Liquid Photo Image-able solder mask, color Red, per IPC-SM-840, Class H, to both sides of the board over bare copper.
   2. All exposed conductive pattern areas not covered with soldermask or other plating shall be plated with Electroless Nickel Immersion Gold, ENIG, per IPC-4552, suitable for wire-bonding.
   3. Solder mask images that are the same size as the component pads may be enlarged, but shal not be enlarged beyond 0.05mm per side. All other soldermask shall not be enlarged.
   4. Soldermask overlap on smd lands is not allowed.

5 MARKING
   1. UL logo, manufacturer's identification and date code shall be rendered on the top side of the pcb, in silkscreen.
   2. Silkscreen both sides of the pcb using non-conductive expoxy ink, color white. No ink shall be on exposed pads.

6 TEST REQUIREMENTS
   1. 100% netlist verification using the netlist information in the ODB++ data

Min. Track/Gap (Outher)	:	0.10/0.10
Min. Track/Gap (Inner)	:	0.094/0.10
SMD Pads Top	:	1432
SMD Pads Bottom	:	420
Minimum Pitch	:	0.50mm
Minimum Annular Ring	:	0.25mm
Total no. of holes	:	2604
Largest hole size	:	3.2
Smallest hole size	:	0.25
No. of hole sizes	:	13
Connector Gold	:	No
Peel-Off mask	:	No
Scoring	:	No
Internal routing	:	No
Manufacturer's Logo	:	Yes, See drawing PCB-002
Date Marking	:	Yes, See drawing PCB-002
UL Marking	:	Yes, See drawing PCB-002

REV.	BY	NOTES

100MHZ IN THE LOOP V3.0

PROJECT : ET PATHFINDER

SHEET NAME : PCB SPECIFICATION

ET nr. : 11500.04.01.3

Designed by : charli

Drawn by : charli

SHEETSIZE : 420 X 297 MM

SHEET 1 OF 3 | A3

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Date 2023-12-15

Sheet Number PCB-001

REV.	BY	NOTES

100MHZ IN THE LOOP V3.0

PROJECT : ET PATHFINDER

SHEET NAME : PCB DIMENSIONS

ET nr. : 11500.04.01.3

Designed by : charli

Drawn by : charli

SHEETSIZE : 420 X 297 MM

SHEET 2 OF 3 | A3

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Date 2023-12-15

Sheet Number PCB-002

Through Holes
All Drills (unless specified) +/- 0.10 (MM)

Symbol	Diameter	Tolerance	Plated	Quantity
A	0.25	+/- 0.05	Yes	2292
B	0.80		Yes	14
C	0.90		Yes	30
D	0.90	+/- 0.08	Yes	42
E	1.00	+/- 0.08	Yes	10
F	1.00	+/- 0.10	Yes	31
G	1.20		Yes	1
H	1.20	+/- 0.08	Yes	6
J	1.30	+/- 0.08	Yes	3
K	1.50		Yes	6
L	1.70		Yes	112
M	2.00		Yes	18
N	2.50		Yes	2
P	2.80		Yes	4
R	3.10	+/- 0.08	Yes	8
T	3.20		Yes	23
U	3.20	+/- 0.08	Yes	2
V	2.00		No	3

REV.	BY	NOTES

100MHZ IN THE LOOP V3.0

PROJECT : ET PATHFINDER

SHEET NAME : DRILL CHART

ET nr. : 11500.04.01.3

Designed by : charli

Drawn by : charli

SHEETSIZE : 420 X 297 MM

SHEET 3 OF 3 | A3

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Sheet Number PCB-003